

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

NISHIKAWA et al.

Serial No.: 10/786,296

Filing Date: February 26, 2004

For: SEMICONDUCTOR INTEGRATED
CIRCUIT DEVICE CONFIGURED TO
PREVENT THE GENERATION OF A
REVERSE CURRENT IN A MOS
TRANSISTOR

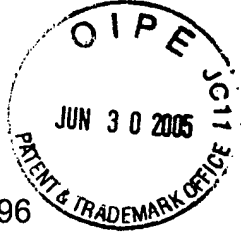
Customer No. 004372

Confirmation No.: 3437

Art Unit: 2811

Examiner: Ori NADAV

Docket No.: 103213-00072



PETITION FOR EXTENSION OF TIME

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

June 30, 2005

Sir:

Applicants petition for an extension of time for one-month for response to the
outstanding Office Action dated March 15, 2005 from June 15, 2005 to July 15, 2005.

Attached is our check in the amount of \$120.00.

The Commissioner is hereby authorized to charge any fee deficiency or credit
any overpayment associated with this communication to Deposit Account No. 01-2300,
referencing docket number 103213-00072.

Respectfully submitted,


Sam Huang
Registration No. 48,430

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120.00 DP

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